

Recardis *Solutions for IC Design Experts*

Press Release

Friday, February 13th, 2004

Nuremberg / Germany:

Regtify to be Integrated into Design Flow at Lucent Technologies Network Systems GmbH in Nuremberg

Joachim Knäblein, Chief Product Development:

“Regtify is a framework for specification, documentation, code generation and verification of HW/SW interfaces. It greatly supports the user in capturing register data and generates RTL-VHDL code, documentation in hyperlinked PDF, software drivers and other related information. Providing enormous flexibility and customizability Regtify’s results can be tailored according to the motto: 'A tool is to adapt to the user and not the other way around'. Thus it fits in everyone’s design flow.”

Hans Sahm, CAD Governor, Lucent Technologies:

“Developing highest-complexity ASICs and FPGAs with thousands of registers being accessed by HW and SW requires a consistent tool flow from specification, implementation and verification to device validation. An integrated solution for this task supporting registers, RAM and multi chip environments is absolutely mandatory for us. We are convinced that Regtify has the potential to fulfill all our requirements. Therefore we are currently evaluating the tool with the aim of integrating it into our ASIC and FPGA flow.”

About Lucent Technologies

Lucent Technologies designs and delivers the systems, services and software that drive next-generation communications networks. Backed by Bell Labs research and development, Lucent uses its strengths in mobility, optical, software, data and voice networking technologies, as well as services, to create new revenue-generating opportunities for its customers, while enabling them to quickly deploy and better manage their networks. Lucent's customer base includes communications service providers, governments and enterprises worldwide. For more information on Lucent Technologies, which has headquarters in Murray Hill, N.J., USA, visit www.lucent.com.

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